UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 7,415,643 B2

Page 1 of 1

APPLICATION NO. : 10/635371

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INVENTOR(S)

: Richard W. Adkisson

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 1, line 32, below "entirety." insert -- Related subject matter disclosed in the following commonly owned co-pending U.S. patent applications: (i) A BUS INTERFACE MODULE, filed March 28, 2003; Application No. 10/402,092; and (ii) AN INTEGRATED CIRCUIT, filed March 28, 2003; Application No. 10/402,034, is hereby incorporated by reference. --.

In column 7, line 23, delete "coverage mode" and insert -- coverage_mode --, therefor.

Signed and Sealed this

Second Day of December, 2008

JON W. DUDAS Director of the United States Patent and Trademark Office